



제 30회 한국반도체학술대회

The 30th Korean Conference on Semiconductors

2023년 2월 13일(월)~ 15일(수) | 강원도 하이원리조트(그랜드호텔 컨벤션타워)

2023년 2월 14일(화), 09:00-10:30

Room K (다이아몬드 I, 6층)

S. Chip Design Contest 분과

[TK1-S] Chip Design Contest

좌장: 심재훈 교수(경북대학교), 차혁규 교수(서울과학기술대학교)

TK1-S-1 09:00-09:15	An 81.2dB-SNDR Dual-Residue Pipeline ADC with a 2nd-Order Noise-Shaping Interpolating-SAR ADC Jae-Hyun Chung and Seung-Tak Ryu KAIST
TK1-S-2 09:15-09:30	Energy-Efficient Neural Network Processor Using All-Analog Computation Jin-O Seo and SeongHwan Cho KAIST
TK1-S-3 09:30-09:45	Single-Chip THz Imaging System with Trantenna Sang Hyo Ahn ¹ , Minjae Kim ¹ , Myoung Kim ¹ , Yoo Bin Song ¹ , Min Woo Ryu ^{1,2} , and Kyung Rok Kim ^{1,2} ¹ Department of Electrical Engineering, UNIST, ² Terrell Corp.
TK1-S-4 09:45-10:00	A Spike Detection Based Wireless Neural Signal Recording System Joonyoung Lim, Chae-Eun Lee, Yu-Ri Kim, Chieun Choi, and Yoon-Kyu Song Graduate School of Convergence Science and Technology, Seoul National University
TK1-S-5 10:00-10:15	NAND 플래시 메모리와 SRAM이 융합된 NAS 메모리 안지훈 ¹ , 구민석 ² , 김윤 ¹ ¹ 서울시립대학교 전자전기컴퓨터공학과, ² 인천대학교 컴퓨터공학부
TK1-S-6 10:15-10:30	A 19.8W/29.6W Hybrid Step-Up/Down DC-DC Converter with 97.2% Peak Efficiency for 1-Cell/2-Cell Universal Battery Charger Applications Seongil Yeo ¹ , Uyong Hyeon ^{1,2} , Mingyeong Kim ^{1,2} , and Kunhee Cho ¹ ¹ Kyungpook National University, ² Samsung Electronics Co., Ltd.