



제 30회 한국반도체학술대회

The 30th Korean Conference on Semiconductors

2023년 2월 13일(월)~ 15일(수) | 강원도 하이원리조트(그랜드호텔 컨벤션타워)

2023년 2월 14일(화), 10:55-12:40

Room A (에메랄드 I, 5층)

D. Thin Film Process Technology 분과

[TA2-D] Memory Devices

좌장: 한정환 교수(서울과학기술대학교), 전우진 교수(경희대학교)

<p>TA2-D-1 10:55-11:25 [초청]</p>	<p>Optimization of Self-rectifying Resistive Switching Memory for Application in Hole-etched Vertical Array Configuration Hae Jin Kim <i>Department of Electronic Materials Engineering, The University of Suwon</i></p>
<p>TA2-D-2 11:25-11:40</p>	<p>Growth of High-k Rutile TiO₂ Thin Films on SnO₂ Layers Using Atomic Layer Deposition for DRAM Capacitors Daeun Lim¹, Yeji Lee², Jonghyun Kim², Jina Kim³, Jeong Hwan Han³, Eun A Kim², Seong-Yong Cho², and Woongkyu Lee¹ <i>¹Soongsil University, ²Myongji University, ³Seoul National University of Science and Technology</i></p>
<p>TA2-D-3 11:40-11:55</p>	<p>Study on Electro-thermal Behavior of Ovonic Threshold Switch by In Situ Thermal Imaging Ju Hwan Park¹, Myeong Jun Jung¹, Ha Young Lee¹, Gun Hwan Kim², Min Kyu Yang³, and Byung Joon Choi¹ <i>¹Seoul National University of Science and Technology, ²KRICT, ³Sahmyook University</i></p>
<p>TA2-D-4 11:55-12:10</p>	<p>The Controlled Nitrogen Profile and Amount in SiO₂ Thin Film Using Remote Plasma Oxidation and Nitridation for DRAM Application Moonsuk Choi¹, Chaewon Kim¹, Sunbum Kim¹, Ji Hyeon Sim¹, Hyeongjun Kim¹, Juhwan Kim², Junwoo Park², Pilseong Jeong², Sanghyun Ji³, and Changhwan Choi¹ <i>¹Division of Materials Science and Engineering, Hanyang University, ²Software Team, AP Systems, ³Semiconductor Equipment Department, AP Systems</i></p>
<p>TA2-D-5 12:10-12:25</p>	<p>Improved Energy Storage Performance of the Al-doped ZrO₂ with Antiferroelectricity for Electrostatic Capacitors Seung Won Lee, Youkyoung Oh, Min Ji Jeong, and Ji-Hoon Ahn <i>Department of Materials Science and Chemical Engineering, Hanyang University</i></p>
<p>TA2-D-6 12:25-12:40</p>	<p>Crystallinity-dependent Low Current and Analog Switching Behavior of Ru Ion-Based Memristor Ji Eun Kim^{1,2}, Jae Uk Kwon^{1,2}, Suk Yeop Chun^{1,3}, Young Geun Song¹, Doo Seok Jeong⁴, Chong-Yun Kang^{1,3}, Seong Keun Kim^{1,3}, Sahn Nahm^{2,3}, and Jung Ho Yoon¹ <i>¹Electronic Materials Research Center, KIST, ²Department of Materials Science and Engineering, Korea University, ³KU-KIST Graduate School of Converging Science and Technology, Korea University, ⁴Division of Materials Science and Engineering, Hanyang University</i></p>