2023년 2월 15일(수), 10:45-12:30
Room G (스페이드 II+III, 6층)

K. Memory (Design & Process Technology) 분과
[WG2-K] Processing In Memory

좌장: 정두석 교수(한양대학교), 김윤 교수(서울시립대학교)

WG2-K-1 **10:45-11:15** **[초청]**	**Memory-Based Hardware Neural System for High-density and Low-power Applications** Min-Hwi Kim *Chung-Ang University*
WG2-K-2 **11:15-11:30**	**저전력 추론을 위한 용량성 커플링 기반의 뉴로모픽 아키텍처** Jung Nam Kim[1], Minsuk Koo[2], and Yoon Kim[1] *[1]Department of Electrical and Computer Engineering, University of Seoul, [2]Department of Computer Science and Engineering, Incheon National University*
WG2-K-3 **11:30-11:45**	**Stochastic Stateful Logic Technology and Its Application to Evolutionary Learning Algorithm in TaO_x-Memristor Crossbar** Do Hoon Kim and Kyung Min Kim *KAIST*
WG2-K-4 **11:45-12:00**	**A 256×64 12T SRAM Compute-In-Memory Macro with In-SRAM Reference Voltage Generation** Hyeyeong Lee, Kyeongho Lee, Yeseul Kim, and Jongsun Park *Department of Electrical Engineering, Korea University*
WG2-K-5 **12:00-12:15**	**Probabilistic Computing Hardware based on a Diffusive Memristor** Jaehyun Kim[1,2], Janguk Han[1,2], Kyung Seok Woo[1,2], and Cheol Seong Hwang[1,2] *[1]Department of Materials Science and Engineering, Seoul National University, [2]Inter-university Semiconductor Research Center, Seoul National University*
WG2-K-6 **12:15-12:30**	**Implementation of the 2T-2M Block-Based Reconfigurable Logic Circuit by Combining the Amorphous InGaZnO ReRAM and Thin-film Transistors** Jung Rae Cho, Jingyu Park, Tae Jun Yang, Jong-Ho Bae, Sung-Jin Choi, Dong Myong Kim, and Dae Hwan Kim *School of Electrical Engineering, Kookmin University*