



# 제 30회 한국반도체학술대회

The 30th Korean Conference on Semiconductors

2023년 2월 13일(월)~ 15일(수) | 강원도 하이원리조트(그랜드호텔 컨벤션타워)

2023년 2월 15일(수), 16:00-17:45

Room A (에메랄드 I, 5층)

## L. Analog Design 분과 [WA3-L] Analog Design

좌장: 엄지용 교수(금오공과대학교), 정영호 교수(대구대학교)

<p><b>WA3-L-1</b> 16:00-16:30 [초청]</p>	<p><b>Design of Fully-Integrated Low Dropout Regulator with Wide PSR Range</b> Young-Ha Hwang, Hee-Cheol Joo, and Yeri Kim <i>Soongsil University</i></p>
<p><b>WA3-L-2</b> 16:30-16:45</p>	<p><b>A 99.3dB DR, 15.4<math>\mu</math>W Discrete-time 2<sup>nd</sup> Order Delta-sigma Modulator</b> Byeong-Ho Yu, Jun-Ho Boo, Jae-Geun Lim, Hyoung-Jung Kim, Jae-Hyuk Lee, and Gil Cho Ahn <i>Department of Electronic Engineering, Sogang University</i></p>
<p><b>WA3-L-3</b> 16:45-17:00</p>	<p><b>Validation and Analysis of Stochastic Adaptation Methodology Using a 32Gb/s RC-degenerated CTLE</b> Suil Kang<sup>1</sup> and Kwanso Park<sup>2</sup> <i><sup>1</sup>Kwangwoon University, <sup>2</sup>Yonsei University</i></p>
<p><b>WA3-L-4</b> 17:00-17:15</p>	<p><b>FMCW LiDAR에서 DBR Laser의 주파수를 선형으로 변조시키는 Electro-Optical Phase Locked Loop(EO-PLL)</b> 김의근, 이승주, 박종민, 양성훈, 정재훈, 조영원, 조요셉, 채종혁, 범진욱 <i>Department of Electronic Engineering, Sogang University</i></p>
<p><b>WA3-L-5</b> 17:15-17:30</p>	<p><b>Analysis of 2x Oversampling Phase and Frequency Detector</b> Dongwoo Kang<sup>1</sup> and Kwanso Park<sup>2</sup> <i><sup>1</sup>Kwangwoon University, <sup>2</sup>Yonsei University</i></p>
<p><b>WA3-L-6</b> 17:30-17:45</p>	<p><b>Ultrasonic Preamplifier with Dynamic Push-Pull Current-Reuse Amplifier</b> Min-Hyeong Son and Ji-Yong Um <i>Department of Medical IT Convergence Engineering, Kumoh National Institute of Technology</i></p>