



제 30회 한국반도체학술대회

The 30th Korean Conference on Semiconductors

2023년 2월 13일(월) ~ 15일(수) | 강원도 하이원리조트(그랜드호텔 컨벤션타워)

2023년 2월 15일(수), 16:00-17:45

Room H (하트 I, 6층)

N. VLSI CAD 분과

[WH3-N] Design Automation in Advanced Technology Nodes

좌장: 송대건 교수(경북대학교), 강석형 교수(POSTECH)

<p>WH3-N-1 16:00-16:30 [초청]</p>	<p>Applications of Deep Learning on EDA: Status and Challenges Heechun Park Kookmin University</p>
<p>WH3-N-2 16:30-16:45</p>	<p>MLB: Multi-Vth Tuning for Leakage Power Reduction via Bayesian Optimization Jaeseung Lee, Jakang Lee, Seonghyeon Park, and Seokhyeong Kang POSTECH</p>
<p>WH3-N-3 16:45-17:00</p>	<p>Optimizing Pin Accessibility of 3nm GAAFET Standard Cells Jaehoon Jeong¹ and Taigon Song^{1,2} ¹School of Electronic and Electrical Engineering, Kyungpook National University, ²School of Electronics Engineering, Kyungpook National University</p>
<p>WH3-N-4 17:00-17:15</p>	<p>전력분배망 최적화를 위한 고속 전압 강하 분석 송정식, 오제영, 현대준 청주대학교 전자공학과</p>
<p>WH3-N-5 17:15-17:30</p>	<p>Open-Source 3D Global Placer with D2D Vertical Connections Using Louvain Clustering and RePIAce Minjae Kim, Dohun Kim, and Seokhyeong Kang POSTECH</p>
<p>WH3-N-6 17:30-17:45</p>	<p>A Novel Processing Unit and Architecture for Process-In Memory (PIM) in NAND Flash Memory Hyunwoo Kim¹ and Taigon Song^{1,2} ¹School of Electronic and Electrical Engineering, Kyungpook National University, ²School of Electronics Engineering, Kyungpook National University</p>