



# 제 30회 한국반도체학술대회

The 30th Korean Conference on Semiconductors

2023년 2월 13일(월)~ 15일(수) | 강원도 하이원리조트(그랜드호텔 컨벤션타워)

2023년 2월 15일(수), 16:00-17:45

Room L (다이아몬드 II, 6층)

## G. Device & Process Modeling, Simulation and Reliability 분과 [WL3-G] Thin Film and Memory Devices

좌장: 나현철 상무(DB 하이텍), 홍성민 교수(GIST)

<b>WL3-G-1</b> <b>16:00-16:15</b>	<b>On the Scalability of the Amorphous InGaZnO Field-effect Transistors Compared with Silicon-on-insulator Field-effect Transistors</b> Ho Jung Lee, Donguk Kim, Jong-Ho Bae, Sung-Jin Choi, Dong Myong Kim, and Dae Hwan Kim <i>School of Electrical Engineering, Kookmin University</i>
<b>WL3-G-2</b> <b>16:15-16:30</b>	<b>Characterization of Photovoltaic and Photoconductive Responses in Amorphous Oxide Semiconductor Thin-Film Transistors</b> Seung Hyeop Han, Han Bin Yoo, Haesung Kim, Jihee Ryu, Ju Young Park, Jong-Ho Bae, Sung-Jin Choi, Dae Hwan Kim, and Dong Myong Kim <i>Kookmin University</i>
<b>WL3-G-3</b> <b>16:30-16:45</b>	<b>Characterization of Lateral Trap Distribution in AOS TFTs through Capacitance-Voltage Technique Combined with Extended Channel Conduction Factor</b> Han Bin Yoo, Haesung Kim, Jihee Ryu, Ju Young Park, Seung Hyeop Han, Hyo-In Yang, Jong-Ho Bae, Sung-Jin Choi, Dae Hwan Kim, and Dong Myong Kim <i>Kookmin University</i>
<b>WL3-G-4</b> <b>16:45-17:00</b>	<b>Development of In-House Device Simulator for NAND Flash Memories</b> Sang-Mok Jeong and Sung-Min Hong <i>School of Electrical Engineering and Computer Science, GIST</i>
<b>WL3-G-5</b> <b>17:00-17:15</b>	<b>Retention Characteristics with Cross-Temperature Effects in 3-D NAND Flash Memory</b> Ukju An, Gilsang Yoon, Donghyun Go, Jounghun Park, Donghwi Kim, Jongwoo Kim, and Jeong-Soo Lee <i>Department of Electrical Engineering, POSTECH</i>
<b>WL3-G-6</b> <b>17:15-17:30</b>	<b>Statistical Distribution of DRAM Retention Time due to Geometric Fluctuation</b> Geonho Park and Sung-Min Hong <i>School of Electrical Engineering and Computer Science, GIST</i>
<b>WL3-G-7</b> <b>17:30-17:45</b>	<b>Fault-tolerant RRAM-Based Convolutional Kernel Using Hybrid Precision Quantization for Image Processing</b> Seonuk Jeon <sup>1</sup> , Eunryeong Hong <sup>2</sup> , Heebum Kang <sup>2</sup> , Hyun Wook Kim <sup>2</sup> , Nayeon Kim <sup>1</sup> , and Jiyong Woo <sup>1,2</sup> <i><sup>1</sup>School of Electronics Engineering, Kyungpook National University, <sup>2</sup>School of Electronic and Electrical Engineering, Kyungpook National University</i>